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09/835080  
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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER  
1:401

U.S. **UTILITY** Patent Application

F#                      O.I.P.E.                      PATENT DATE                       
 SCANNED IR3 Q.A.                     

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/835080	F	861	458	2835	NAKAKURA

## APPLICANTS

Katsuya Sakayori:

# Title

Laminate for electronic circuit

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Local Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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